

ABSTRACT

This method of controlling access, by a processor to a peripheral of this processor, includes the following steps:

- triggering (E34) a processor interrupt, termed control interrupt;
- obtaining (E37), from the processor and following this triggering, an access authorisation code (Code-AA) to the peripheral;
- comparing (E38) the access authorisation code (Code-AA) with a predetermined reference value (Code-UMCA);
- generating (E50) an electrical signal validating a peripheral access signal, depending on the outcome of said comparison step (E30).

Figure 3.